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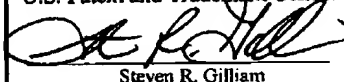
<b>Title:</b> APPARATUS, SYSTEM AND METHOD FOR VALIDATING INTEGRITY OF TRANSMITTED DATA	
<b>Application No.:</b> 09/879,575	<b>Filed:</b> June 12, 2001
<b>Examiner:</b> Ellen C. Tran	<b>Group Art Unit:</b> 2134
<b>Atty. Docket No.:</b> 037-0039 (formerly 1999-0275)	<b>Confirmation No.:</b> 4755

**ATTACHED HERETO:**

- (1) Applicant's Interview Summary (2 pages)

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I hereby certify that, on the date shown below, this correspondence is being facsimile transmitted to the U.S. Patent and Trademark Office.

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Steven R. Gilliam Date

Respectfully submitted,



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OCT 03 2006

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): James Alexander Reeds

Title: APPARATUS, SYSTEM AND METHOD FOR VALIDATING  
INTEGRITY OF TRANSMITTED DATA

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October 3, 2006

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**APPLICANT'S INTERVIEW SUMMARY**

This paper is being submitted to summarize content of the interview conducted on September 20, 2006.

Any fees required by this paper are being provided as directed in an electronic submission of this paper or in a transmittal letter accompanying this paper. However, the Commissioner is hereby authorized to charge any deficiency in fees required by this paper and any additional fees under 37 C.F.R. § 1.16 or 1.17 which may be required during the pendency of this application, and to similarly credit any overpayment, to Deposit Account 50-0631.